



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re the Application of: Yoshitaka UEDA et al.

Group Art Unit: 2816

Serial Number: 10/760,475

Examiner: An T. Lun

Filed: January 21, 2004

Confirmation Number: 9800

**For: SEMICONDUCTOR INTEGRATED CIRCUIT RELATED TO A  
CIRCUIT OPERATING ON THE BASIS OF A CLOCK SIGNAL**

Attorney Docket Number: 001681A

Customer Number: 38834

**AMENDMENT UNDER 37 C.F.R. §1.312**

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

January 27, 2006

Sir:

In response to the Notice of Allowance dated January 13, 2006, Applicants herein amend the above-identified application as follows and submits the following remarks.

Amendments to the Specification begin on page 2 of this paper.

Remarks/Arguments begin on page 3 of this paper.

OK to enter  
4/18/06  
AK